

Notification for Recruitment for Junior Research Fellow
Department of Electronics and Communication Engineering
Project funded by Chips to Startup Program, Ref: IIITA/C2S/2023/01

- **Project Areas:** CMOS Receiver Chip Design, fabrication and Test for Indian-Long Range Frequency band.
- **Required Skills:**
 - a. Strong Analog/RF Integrated Circuit Design skills using Cadence EDA tools.
 - b. Good knowledge of MOSFETs.
- **No. of Positions:** One
 - a. Junior Research Fellow/Project Associate (One)
- **Salary Particulars:** The monthly remuneration for this position will be as follows.
 - a. Junior Research Fellow: 37000 Rs (Thirty Seven Thousand Rupees Only).
- **Eligibility for JRF:** B.E./B.Tech. or ME/MTech from reputed institutions with specialization in areas allied to the advertised project areas and with minimum of 60% marks.
- **Appointment Duration:** Initially for 6 month with the possibility of an extension depending on performance and availability of funding upto one year.

Hostel accommodation may be provided at the institute based on availability on a chargeable basis. In case of non-availability of institute hostel, admissible HRA will be provided as per Government of India rules.

- **Deadline for application:** 17 August 2026 and **Date of Interview:** 24 August 2026

Candidates satisfying the required skills may send their resumes and certificates pertaining to educational qualifications starting from class X (in a single PDF) to “prasanna@iiita.ac.in”. The subject of the e-mail application should be **Application for JRF- IIITA/C2S/2023/01**. The candidate is expected to join immediately after receiving the offer letter.