



**Department of Electronics and Communication Engineering**  
**Indian Institute of Information Technology Allahabad**  
**Allahabad-211012 (UP) India**

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Advt. No. IIITA/ECE/SS/PRJ-CRG-03

Dated -09/02/2023

Last date of application submission-10/03/2023

**Re-advertisement for the Post of Junior Research Fellow (JRF) (On Contract)**

Applications are invited for the post of Junior Research Fellow (JRF) (on contract) in a research project titled “**Design and Implementation of Biocompatible Energy Harvesting Device for Low Frequency Operation**” in reference to the File No. CRG/2022/009226 dated: 27/12/2022 by Science and Engineering Research Board (SERB), Department of Science & Technology, New Delhi-110070. The duly completed application in the prescribed format along with copies of supporting documents must reach to **Dr. Sunny, Department of Electronics & Communication Engineering, Room No. 2106, Computer Centre–1, Indian Institute of Information Technology Allahabad, Deoghat, Jhalwa, Allahabad, Uttar Pradesh 211012 on or before 10/03/2023** with the subject as “**Application for the post of Junior Research Fellow (JRF) (on contract) in the SERB Research Project- File No. CRG/2022/009226**”. A soft copy of the application should also be sent to [sunnys@iiita.ac.in](mailto:sunnys@iiita.ac.in) (PI of the project).

**Title of the Project:** Design and Implementation of Biocompatible Energy Harvesting Device for Low Frequency Operation

**Name of Position:** Junior Research Fellow (JRF)

**Number of Positions:** 01 (one)

**Project Duration:** Three (03) Years

**Age Limit:** Maximum 35 Years (Age relaxation will be applicable as per DST norms)

**Eligibility Criterion:**

- (i) The candidate MUST have secured at least 60% cumulatively or equivalent CGPI in the qualifying degree. A 5% relaxation shall be applicable for SC/ST/EWS/PH Candidates.
- (ii) A B.Tech candidate with a minimum CGPI of 8.5 and who fulfils the desirables of project may be considered for the position.

**Qualifications -**

- (i) For Candidates having B.Tech./ M.Tech. (VLSI / ECE / Material Science / Nanotechnology) Degree with valid GATE Score/valid UGC/CSIR NET or JRF
- (ii) For Candidates having M.Sc. (Electronics Science) or M.Sc. (Physics with specialization in Electronics) with valid GATE Score/valid UGC/CSIR NET or JRF

- (iii) Candidates with a CGPI of more than 8.0 from IITs/ IIITs/ IISERs/ and such institutions of National importance may be called for the selection process directly.

**Desirable:** The applicant may be preferred if s/he has knowledge of Semiconductor Physics, Device simulation / thin film fabrication and surface characterizations, etc. Experience in simulations with CAD software (COMSOL/Ansys/SolidWorks etc.)

**Fellowship:** 31,000/- pm (Fixed) for first two years and 35,000/- pm (Fixed) for the third year.

### **Note(s)**

1. The candidate should be able to travel across various Institutes/Research Centres for the project related work whenever needed.
2. The applicant will be responsible for the authenticity of the information, other documents, and photographs submitted.
3. The institute reserves the right to accept/reject the application at any time, and/or consider candidates of exceptional credentials without applications. Qualification and experience may be relaxed at any point of time for exceptional candidates.
4. Mere, possessing the prescribed qualification does not ensure that the candidate will be called for an Interview. The candidates will be shortlisted on the basis of merit and need of the project.
5. Shortlisted candidates will be informed by e-mail about the interview date. So, the candidates must provide valid e-mail ID(s) in their application.
6. Shortlisted candidates have to present themselves for the interview on the scheduled date with updated CV, original and attested photocopies of mark sheets/certificates in support of their academic qualifications.
7. Applicants in employment (private, government or any other organization) are required to submit a “**No Objection Certificate**” from the employer at the time of interview.
8. No TA/DA will be paid for appearing in the interview.
9. The first and foremost responsibility of the candidate is to fulfill the jobs’ requirements to complete the above mentioned project in timely manner.
10. The association between PI and the candidate can be discontinued at any time by giving one month notice in writing either by the PI of the project to the candidate, or by candidate to the PI of the project.
11. Candidate selected for this position may also apply for the regular Ph.D. program in ECE Department, provided he/she satisfies the eligibility criterion followed by the selection process laid down by the institute (IIIT-Allahabad).
12. Hostel accommodation may be provided as per institute norms

**Dr. Sunny**  
**Principal Investigator (PI)**  
**ECE Department**  
**IIT-Allahabad-211012 (U.P.)-India**



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**For office use only**  
Application No.....

**APPLICATION FORM**

Advt. No. IIITA/ECE/SS/PRJ-CRG-03

**Dated: 09/02/2023**

**Research Project Entitled- “Design and Implementation of Biocompatible Energy Harvesting Device for Low Frequency Operation”**

**File No. :** CRG/2022/009226  
**Funding Agency:** Science and Engineering Research Board (SERB), New Delhi  
**Name of P.I.:** Dr. Sunny  
**Name of position:** Junior Research Fellow (on contract)  
**Department/Section:** Department of Electronics and Communication Engineering

1. Name of the Candidate (In Capital):
2. Sex (Male/Female):
3. Marital Status (Married/Unmarried):
4. Date of Birth:
5. Age (as on last date of application): \_\_\_\_\_ Years \_\_\_\_\_ Months \_\_\_\_\_ Days
6. Father’s/Husband’s Name:
7. Present/Contact Address:
8. Permanent Address:
9. Mobile No:
10. E-mail:
11. Nationality:
12. Category Gen/EWS/SC/ST/OBC/PH:

13. GATE/NET:

Branch	Year	Valid up to	Percentile (& Score)	All India Rank

14. Educational Qualification (10<sup>th</sup> onward):

Qualification	Subject/Discipline	Board/Institute/Univ.	Year of passing	% of marks	Division
10th					
12th					
Diploma, if any					
B.E./B.Tech/M.Sc.					
M.E./M.Tech					

15. Summary of M.E/M.Tech Thesis (Maximum 150 words): Enclose a separate sheet

16. Research Publications (Maximum Best 5): Enclose a separate sheet

17. Details of Experience in chronological order. Enclose a separate sheet

18. Any other information: Enclose a separate sheet

19. Indicate whether you propose to register for higher degree at IITA, Allahabad: Yes/No  
(Name of higher degree is Ph.D.)

**DECLARATION**

I hereby declare that all the statements made in this application are true and complete, and nothing has been concealed/distorted. I am aware that, if at any time I am found to have concealed/distorted any material information, my engagement is liable to be summarily terminated without notice.

Place:

Signature of the Applicant

Date:

**Checklist-**

1. Self-attested copy of documents
2. Proof of Date of Birth
3. Mark sheets & certificates of all the examinations passed
4. Category Certificate, if applicable
5. GATE/NET score card, if passed
6. Experience certificate, if any